





10/12/00

228	214
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER
6585150

6585150

U.S. UTILITY PATENT APPLICATION

 O.I.P.E. SCANNED <u>Am</u> O.A. <u>W</u>	PATENT DATE JUL 01 2003
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APPLICATION NO. 09/687524	CONT/PRIOR	CLASS 228	SUBCLASS 214	ART UNIT 1725	EXAMINER Johnson
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APPLICANTS

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James Spalik
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TITLE

Solder protective coating and fluxless joining of flip chip devices on laminates with plated solder

PTD-2040
12/99

Best Available Copy

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
228		214		228	223				
INTERNATIONAL CLASSIFICATION				427	96				
B	2	3	K	31 / 02		/			
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg. 1	Figs. Drwg. 1	Print Fig. 1	Total Claims 15
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.	<i>Jonath Jh</i> (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED <i>1/27/03</i>	
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	<i>T. Dunn</i> TOM DUNN SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 1700 _____ (Primary Examiner)		ISSUE FEE Amount Due <i>\$1300</i>	
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	<i>Jan</i> (Legal Instruments Examiner)		Date Paid <i>4/21/03</i>	
	<i>3/14/03</i> (Date)		ISSUE BATCH NUMBER	

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(Rev. 6/98)

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(LABEL AREA)

(FACE)

20 K24 Drawing